Serial No. 10/509,969   Docket No. PU020085
Art Unit 2622   Customer No.24498

Listing of the Claims

This listing of claims will replace all prior versions and listings of claims in the application.

1.   (Currently amended) A processing apparatus for video and or audio signals comprising:

a first module having a controller coupled to a signal processor having a signal processing characteristic selected from a plurality signal processing characteristics stored in a non-volatile memory; and,

a second module coupled to said first module, said second module having at least one personality pin carrying a logical potential which provides a unique identification of said second module to said first module and having a specific input output signal coupling characteristic,

said controller comprising means for determining said logical potential on said at least one personality pin input output signal coupling characteristic of said second module, and means for selecting, from said plurality of signal processing characteristics stored in said non-volatile memory, a signal processing characteristic for said signal processor in accordance with said logical potential.

2.   (Currently amended) The apparatus of claim 1, wherein said controller determines said input output signal coupling characteristic logical potential on said at least one personality pin of said second module during a power up sequence.

Cancel Claim 3

Cancel Claim 4

Cancel Claim 5.

Cancel Claim 6.

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7.   (Previously presented) the apparatus of claim 1, wherein said second module comprises only passive electronic circuitry.

8.   (Currently amended) The apparatus of claim 1, wherein others ~~ones~~ of said plurality signal processing characteristics stored in said non-volatile memory correspond with other ~~ones of said~~ second modules, each having different input output signal coupling characteristics.

9.   (Previously presented) The apparatus of claim 1, wherein said second module comprises passive and active electronic circuitry.

10.   (Original) The apparatus of claim 9, wherein said active electronic circuitry is functionally configurable.

11.   (Previously presented) The apparatus of claim 1, wherein said first module has an audio signal processing characteristic.

Cancel Claim 12.

Cancel Claim 13.

Cancel Claim 14.

15.   (Currently amended) A processing apparatus for video and or audio signals comprising:
        a network interface;
        a controller coupled to said network interface;
        a memory coupled to said controller; and,
        a signal processor coupled to said memory, said signal processor having a signal processing characteristic determined in accordance with a characteristic stored in said

memory, said signal processor having at least one personality pin carrying a logical
potential which provides a unique identification of sail signal processor to said network
interface;

said controller comprising means for accessing, in accordance with a signal from
said network interface, from a plurality of characteristics stored in said memory, a
characteristic specific to said ~~processing apparatus~~ signal processor.

16.   (Original) The apparatus of claim 15, wherein access to ones of said stored
plurality of signal processing characteristics is limited to only said specific characteristic.

17.   (Previously presented) The apparatus of claim 15, wherein said plurality of
characteristics stored in said memory enable differing levels of signal processing
complexity by said signal processor.

18.   (Original) The apparatus of claim 15, wherein access to ones of said stored
plurality of signal processing characteristics is in accordance with said processing
apparatus selling price.

19.   (Previously presented) The apparatus of claim 15, where in accordance with a
second signal from said network interface said controller enables unlimited access to ones
of said stored plurality of signal processing characteristics.

20.   (Previously presented) The apparatus of claim 15, wherein said memory
containing said plurality of signal processing characteristics is alterable in accordance
with a second signal from said network interface.

21.   (Previously presented) A method for configuring a multi-function signal
processing apparatus for users requiring less than all available functions, comprising the
steps of:
        storing a signal processing characteristic for each of said available functions; and,

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enabling access to at least a predetermined one of said plurality of signal processing characteristics, all remaining ones of said plurality of signal processing characteristics being non-accessible,

subsequent to said storing and enabling steps, said signal processing apparatus being operable only with said at least predetermined one of said plurality of signal processing characteristics.

22.   (Previously presented) The method of claim 21, comprising the step of:
implementing said enabling step in a field programmable gate array.

23.   (Original) The method of claim 21, wherein said enabling step comprises the step of inhibiting access to all but said at least predetermined one of said plurality of signal processing characteristics.

24.   (Original) The method of claim 21, wherein said enabling step comprises the step of enabling said at least predetermined one of said plurality of signal processing characteristics to be read during a power up condition of said apparatus.